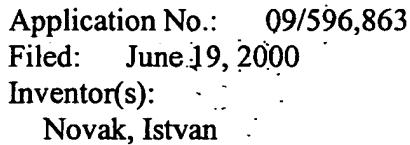


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Title: BYPASS CAPACITOR METHODS FOR ACHIEVING A DESIRED VALUE OF ELECTRICAL IMPEDANCE BETWEEN PARALLEL PLANAR CONDUCTORS OF AN ELECTRICAL POWER DISTRIBUTION STRUCTURE AND ASSOCIATED ELECTRICAL POWER DISTRIBUTION STRUCTURES

Examiner: Bettendorf, J.
Group/Art Unit: 2817
Atty. Dkt. No: 5181-62800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

B. Noel Kivlin

Printed Name _____

January 10, 2002

Signature

Date _____

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Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This paper is submitted in response to the Office Action of October 10, 2001, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

IN THE DRAWINGS:

Submitted herewith is a Request for Approval of Drawing Changes and Figure 11 with the changes noted thereon in red. Applicant respectfully requests approval of the drawing changes.